

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	David H. ASHER et al.	§	Confirmation No.:	3633
		§		
Serial No.:	10/690,137	§	Group Art Unit:	2185
		§		
Filed:	10/21/2003	§	Examiner:	Daniel Y. Kim
		§		
For:	Method And System For	§	Docket No.:	200301840-2
	Absorbing Defects In	§		
	High Performance	§		
	Microprocessor With	§		
	A Large N-Way Set	§		
	Associative Cache	§		

**RESPONSE TO OFFICE ACTION DATED FEBRUARY 7, 2007**

Mail Stop Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Date: April 27, 2007

**AMENDMENT**

Sir:

In response to the Office Action of February 7, 2007, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims that begins on page 2 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.